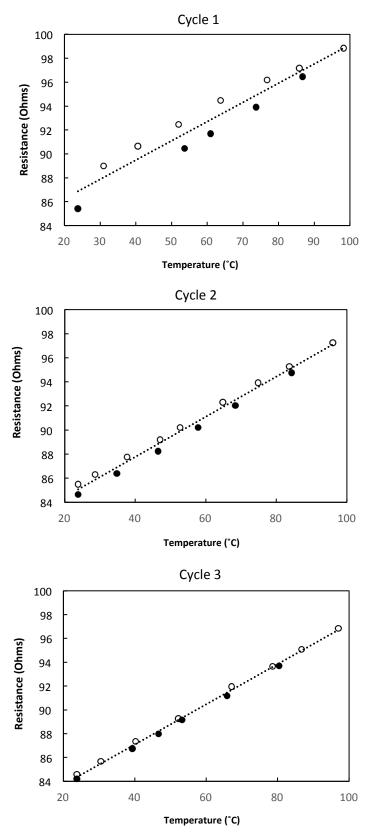
Electronic Supplementary Material (ESI) for Lab on a Chip. This journal is © The Royal Society of Chemistry 2016



SI Figure 1. Measurements of thermistor resistance during temperature cycling between room temperature and 95 $^{\circ}$ C. plots over sequential 3 cycles. Heating (hollow dots) is followed by cooling (filled dots). All chips used in this work were burned in using a minimum of 3 temperature cycles to reduce hysteresis error to below 0.5% over the full PCR temperature range.