Table S1. Percentages of droplets (~60 pL) with 0, 1 or 2 cells.

Figure S1. The process flow of chip fabrication.

Figure S2. (A) Diagram of the chip design. This chip contains three units, including (B) flow focusing droplet generator, (C) droplet splitting region, and droplet accommodation chamber. The channel depth is 50 μ m. Pillar array was designed to support the large accommodation chamber. Scale bar is 150 μ m.