

Supplementary Information

Ionic-Liquid Gated Unipolar Inverters with Tunable Switching Voltage and Excellent Noise Margin

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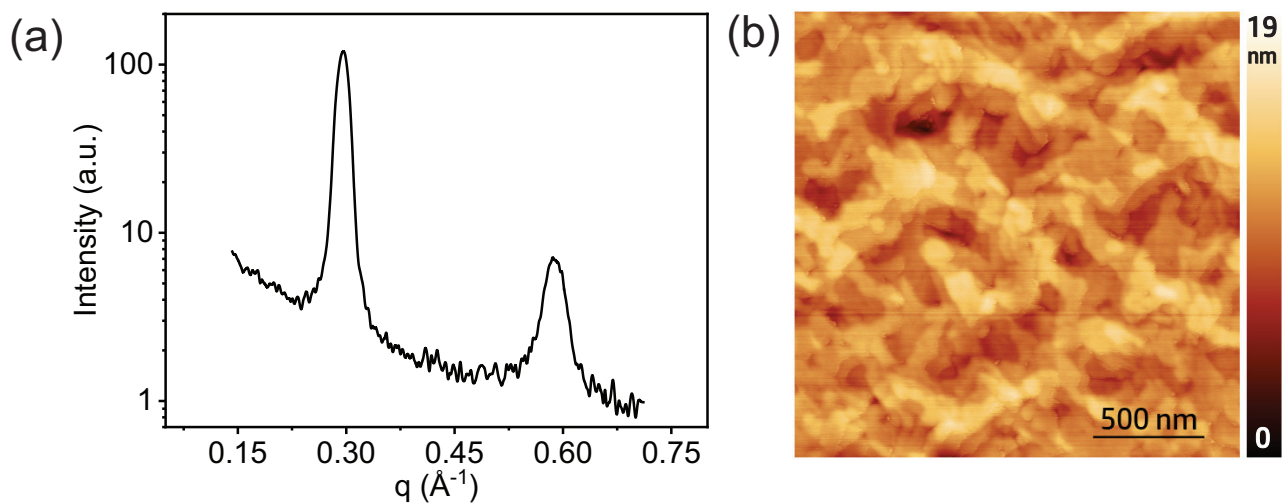


Figure S1: (a) The x-ray diffractogram, and (b) AFM micrograph of the thin film of PBTTT-C14 spin-coated on glass and annealed at 150 °C. The lamellar spacing determined using the XRD peaks is 21.2 Å.

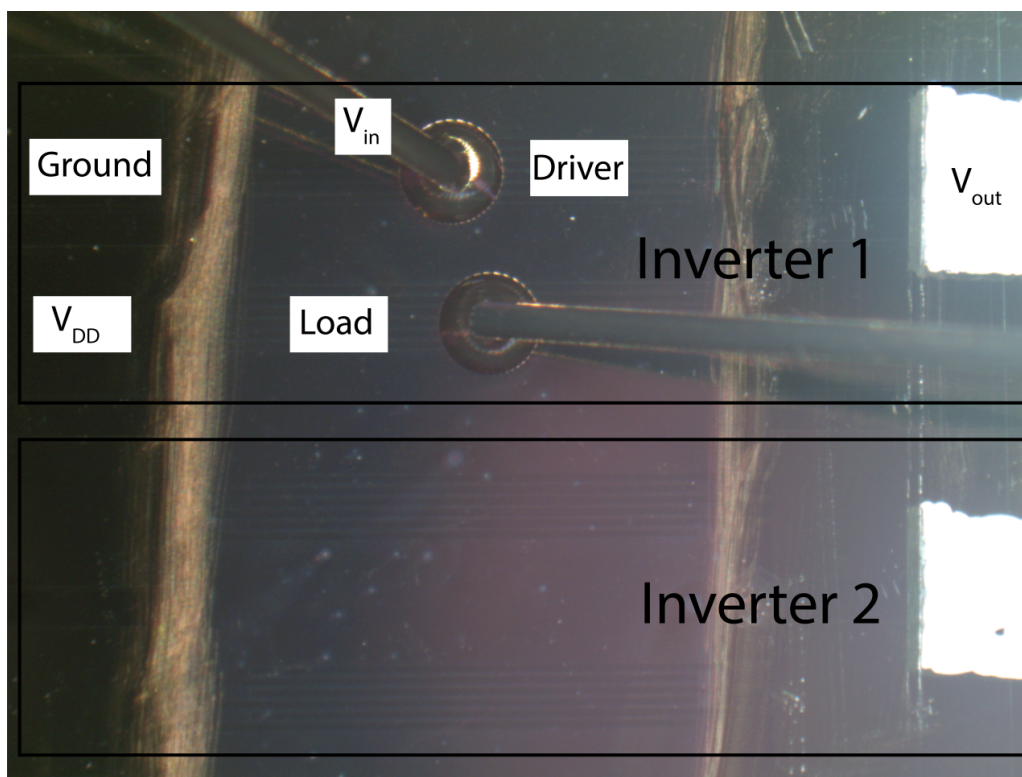


Figure S2: A photo of the IL-gated inverter with different electrical connections labeled. The patch for V_{out} is made using silver paste and connects terminals of the driver and the load together.

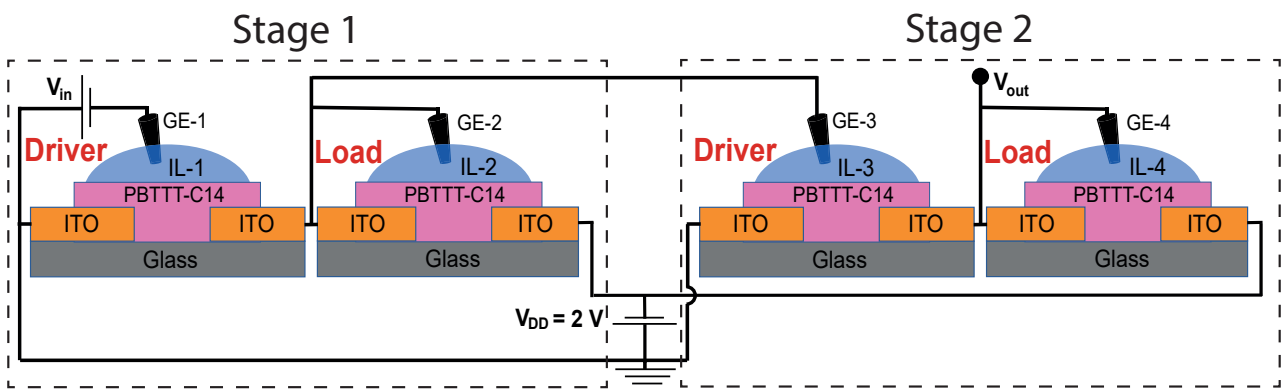


Figure S3: The schematic for the double inverter using IL-gated transistors.