Supporting Information

Integration of Image Preprocessing and Recognition Functions in an Optoelectronic Coupling Organic Ferroelectric Retinomorphic Neuristor

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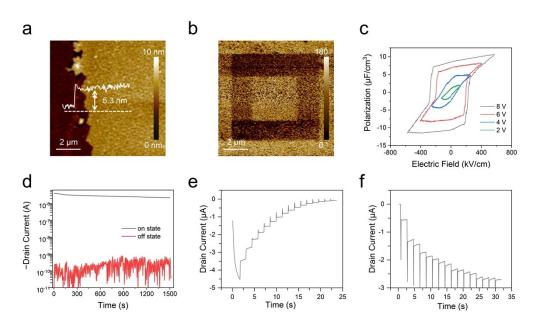


Fig. S1 Atomic force microscope (AFM), piezoresponse force microscopy (PFM), and polarization-voltage (*P*-*E*) loop characterizations of P(VDF-TrFE) thin films, and devices performance characterizations. (a) AFM height image and (b) piezoresponse force microscopy (PFM) out-of-plane phase images of the ultrathin P(VDF-TrFE) film. (c) The *P*-*E* hysteresis loops of P(VDF-TrFE) films with different sweeping ranges. (d) Retention measurements of the currents in the on and off states. (e) Realization of a long-term depression through positive gate pulses (width 10 ms, amplitude 5 V). (f) Realization of a long-term potentiation through negative gate pulses (width 10 ms, amplitude -10 V).

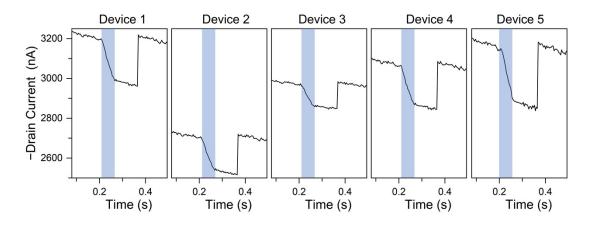


Fig. S2 Device variance of negative photoresponse in 5 different devices under light illumination at $V_d = -1$ V. Shadow areas represent the duration of light illumination.

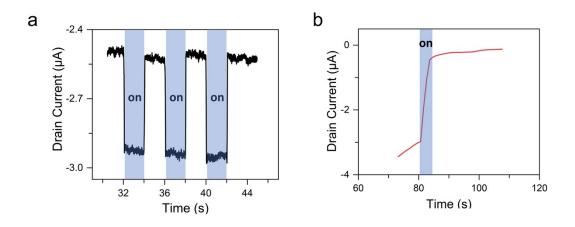


Fig. S3 A control experiment. (a) Photoresponse of the devices with PMMA layer. (b) Photoresponse of the devices without PMMA layer. The inset describes the extraction process of memory window.

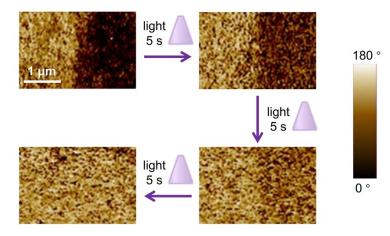


Fig. S4 The PFM phase diagrams of the devices as a function of the light exposure time (wavelengths of 365 nm,

intensity of approximately 10 mW/cm²).

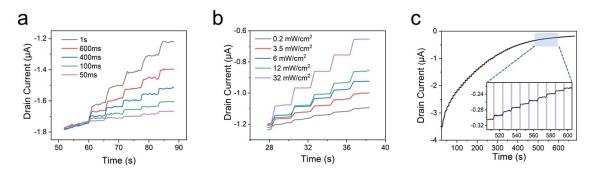


Fig. S5 The multi-level memory characteristics. (a) Time-dependent drain current under five light pulses (365 nm, 3.5 mW/cm²) with different exposure times of 0.05, 0.1, 0.4, 0.6, and 1 s. (b) Time-dependent drain current under five light pulses (365 nm, 400 ms) with different optical powers. (c) Multilevel storage capability characterization (64 states) under light pulses (365 nm, 3.5 mW/cm², 400 ms) every 10 s.

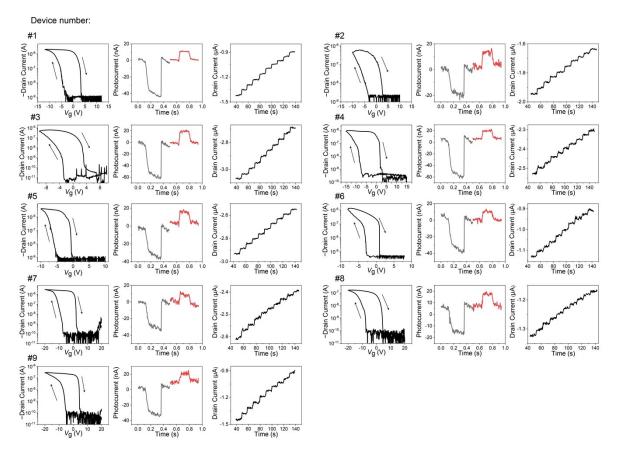


Fig. S6 Transfer curves, bidirectional photoresponse characteristics, and multi-level memory characteristics by optical modulation of the 9 devices.

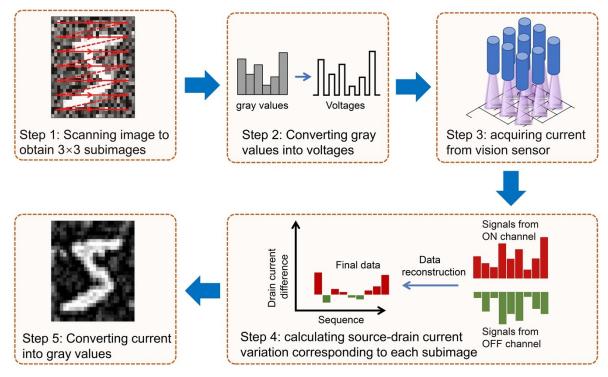


Fig. S7 The specific operation process of image preprocessing.

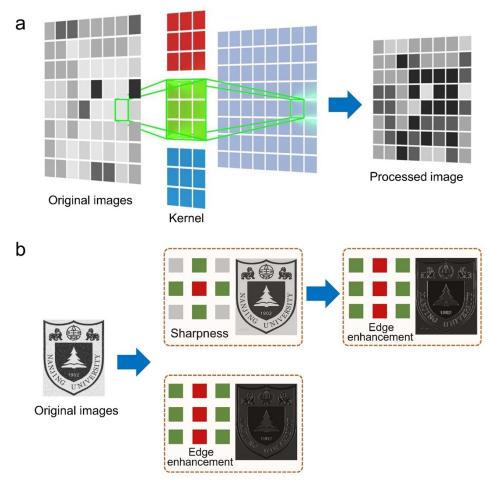


Fig. S8 Reconfigurable retinomorphic vision sensor for simultaneous image sensing and processing. (a) Vision sensor for simultaneous image sensing and processing. (b) Demonstration of image processing with different operations (i.e., edge enhancement and sharpness). These operations are realized by controlling the photoresponse of each pixel in the sensor by varying $V_{\rm g}$ independently.

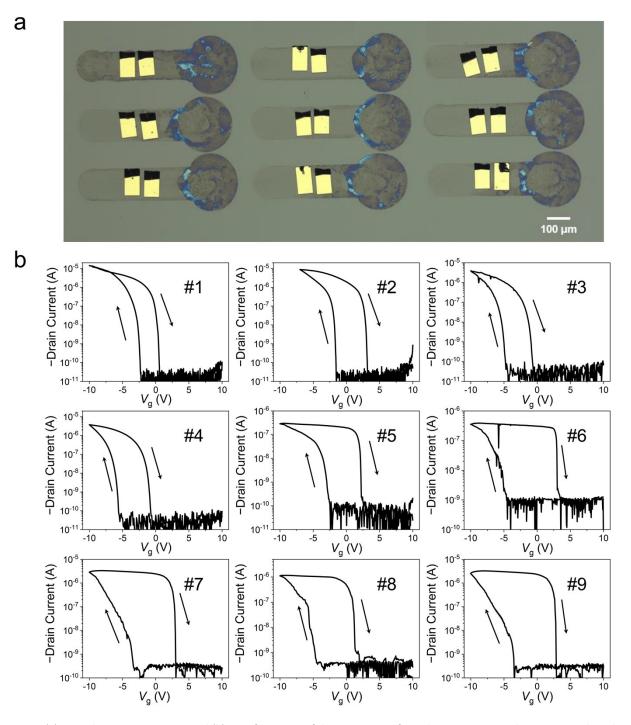


Fig. S9 (a) Optical microscope image and (b) transfer curves of the 3×3 organic ferroelectric retinomorphic neuristors-based sensor array.

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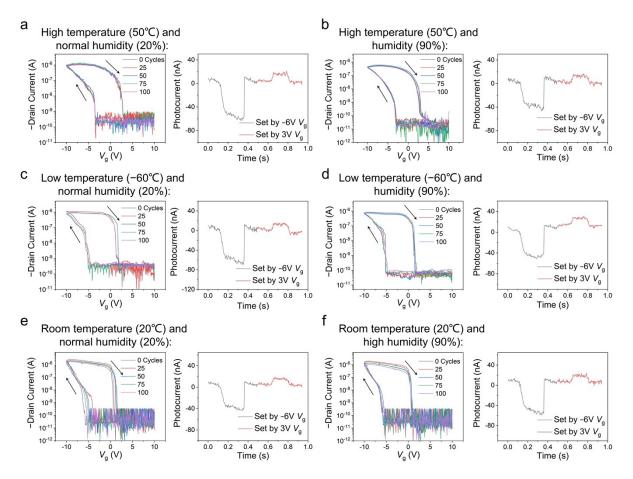


Fig. S10 The stability measurements of our devices encapsulated by the parylene films in harsh environments. The testing time for 100 cycles is one hour. (a) High temperature (50 °C) and normal humidity (20 %), (b) high temperature (50 °C) and humidity (90 %), (c) low temperature (-60 °C) and normal humidity (20 %), (d) low temperature (-60 °C) and humidity (90 %), (e) room temperature (20 °C) and normal humidity (20 %), (f) room temperature (20 °C) and high humidity (90 %).

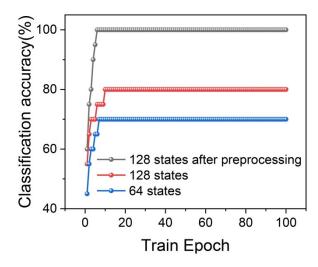


Fig. S11 Comparison of classification recognition rates using different states with and without preprocessing.

Materials system	Structure	Function	Netwo rk type	Accura cy	Ref.
Pyr-GDY/Graphene/Pb	photodiode	recognition	FCNN	90%	1
MoS ₂ /PtTe ₂	transistor	recognition	FCNN	80%	2
Graphene/WSe ₂	transistor	recognition	CNN	72%	3
Graphene/MoS ₂	transistor	recognition	FCNN	92%	4
HfS ₂	transistor	recognition	CNN	88%	5
C ₈ -BTBT/F ₁₆ CuPc	a two-terminal synapse	preprocessing	-	-	6
Graphene/PM6:Y6	transistor	retinomorphic preprocessing	-	-	7
Wse ₂	a two-terminal synapse	retinomorphic preprocessing	-	-	8
PDPP3T:PCBM	transistor	visual adaption	-	-	9
In ₂ O ₃	transistor	visual adaption	-	-	10
MoS ₂	transistor	visual adaption & recognition	CNN	96%	11
α -In ₂ Se ₃	transistor	visual adaption & recognition	CNN	93%	12
MoS ₂ /LSMO	transistor	preprocessing & recognition	CNN	90%	13
ZnO + Mo/SiO ₂ /W	transistor + memristor	preprocessing & recognition	FCNN	99%	14
MoO ₃ /Pd/SiO ₂	random access memory	preprocessing & recognition	CNN	99%	15
BP	transistor	preprocessing & recognition	CNN	92%	16
WSe2/h-BN + HfO _x	transistor + memristor	retinomorphic preprocessing & recognition	CNN	100%	17
C ₈ -BTBT/PMMA/P(VDF- TrFE)	transistor	retinomorphic preprocessing & recognition	CNN	90%	this work

Table S1. Comparison among the recently developed image recognition systems based on different devices. CNN: convolutional neural network; FCNN: full-connected neural network.

Materials and Methods

Fabrication of P(VDF-TrFE) and C₈-BTBT films

 HfO_2 with a thickness of 25 nm was deposited on highly doped n-type Si substrates by atomic layer deposition technology. Then, the substrates were ultrasonically cleaned with acetone, isopropanol and deionized water for 15 minutes. P(VDF-TrFE) (70:30 molar ratio) purchased from Solvay Inc. was dissolved in a mixture of DMF, and the counter solvent p-anisaldehyde (0.5 wt%) at a concentration of 0.5 wt%. 3.5 µL solution was dropped onto the substrate. Moreover, a P(VDF-TrFE) film was obtained with antisolvent-assisted crystallization technology under nitrogen conditions. The substrate with P(VDF-TrFE) was left standing in nitrogen for 12 hours to fully evaporate the solvent. C₈-BTBT (0.5 wt%) and PMMA (0.1 wt%) were dissolved in a mixture of anisole and p-anisaldehyde (0.5 wt%). Subsequently, PMMA/C₈-BTBT thin films were formed on the P(VDF-TrFE) layer through vertical phase separation by the same method.

Device Fabrication

Au electrodes with a thickness of 100 nm and an area of $30 \times 90 \,\mu\text{m}^2$ were transferred onto C₈-BTBT films as source and drain electrodes. The channel length and width of the devices were defined as 80 and 20 μ m, respectively.

Electrical and Optoelectrical Characterizations of Organic Ferroelectric Retinomorphic Neuristors

A semiconductor device analyzer (Agilent B1500) was used to test the electrical characteristics of the devices under vacuum conditions of 10⁻³ Torr. A 365 nm ultraviolet light emitting diode (LED) was used as the light source to measure the optoelectrical performance of the devices, where the light pulse signal was generated by a signal generator. AFM characterizations were performed using a scanning probe microscope (SPA-400) controlled by an SPI 4000 probe station (Seiko Instruments). PFM characterizations were performed with an Asylum Research Cypher scanning probe microscope (Asylum Research, Oxford Instruments, China). ions. *P-E* hysteresis loops were performed with a PREMIER II ferroelectric analyzer.

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