

Supplementary materials

On-receptor computing with classical associative learning in semiconductor oxide memristors

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Table. S1 The comparison of Pt/IGZO/SnO_x/TiN device's nociceptive and synaptic behavior compared to previous works.

No	Structure	Nociceptive function	Synaptic function	Multifunctional behavior	Reference
1	Pt/SiO _x :Ag/ Ag/Pt	Threshold, no-adaptation, relaxation, sensitization	X	X	[S1]
2	Pt/HfO ₂ /Ti N	Threshold, relaxation, sensitization	X	X	[S2]
3	CZO/ITO/gl ass	Threshold, no-adaptation, relaxation, sensitization, recovery	Potentialion and depression	X	[S3]
4	Ag/SiC/Pt	Threshold, no-adaptation, relaxation	STDP	X	[S4]
5	s-ITO/c- ITO	Threshold, no-adaptation, relaxation, sensitization	X	X	[S5]
6	ITO/TiO _x /Ti N	Threshold, no-adaptation, relaxation, sensitization	X	X	[S6]
7	Au/MoS ₂ /A g	Threshold, no-adaptation, relaxation, sensitization	X	X	[S7]
8	Au/CsPbBr ₃ /ITO	X	Potentialion and depression, PPF, STM and LTM	X	[S8]
9	Ti/TaO _x /IT O	X	Potentialion and depression, STDP	X	[S9]
10	Ag/TiO ₂ /Pt	Threshold, no-adaptation, relaxation, sensitization	STM and LTM, SRDP, STDP	X	[S10]
11	Pt/IGZO/Sn O _x /TiN	Threshold, no-adaptation, relaxation, sensitization, recovery	Learning and forgetting, STM and LTM, SRDP	Pavlovian conditioning, reservoir computing	This work

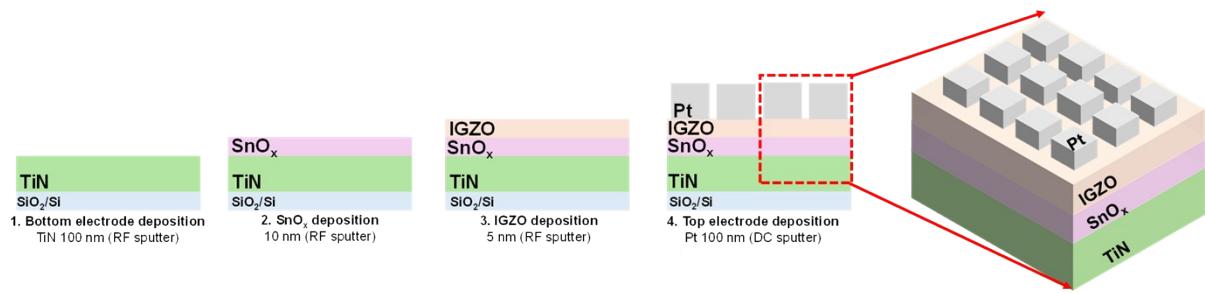


Fig. S1 Fabrication process of the Pt/IGZO/SnO_x/TiN memristor.

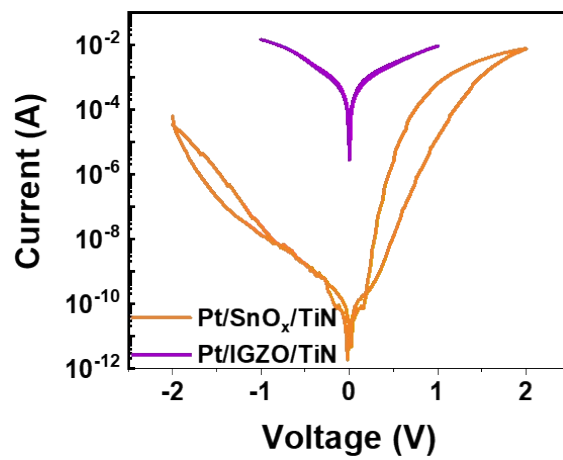


Fig. S2 The I-V curves of Pt/SnO_x/TiN and Pt/IGZO/TiN devices.

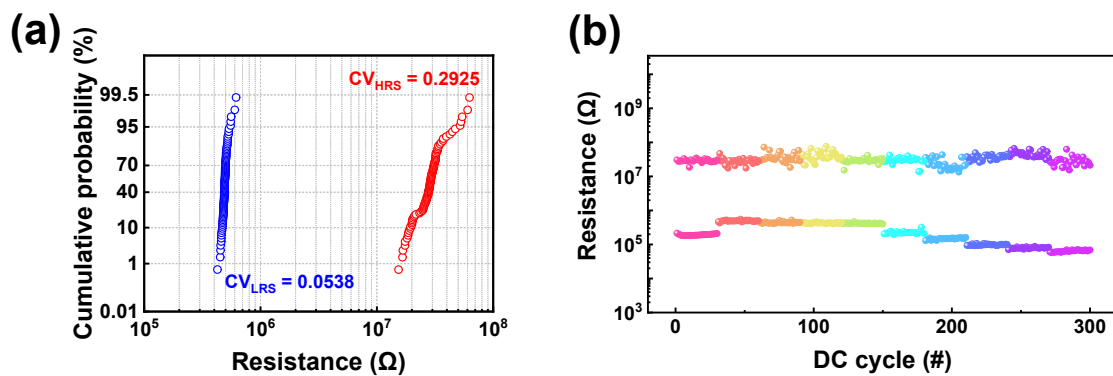


Fig. S3 (a) Coefficient of variation of 100 DC endurance cycles of the Pt/IGZO/SnO_x/TiN memristor. (b) Uniformity of endurance properties over 10 different randomly selected Pt/IGZO/SnO_x/TiN devices, each showcasing 30 cycles.

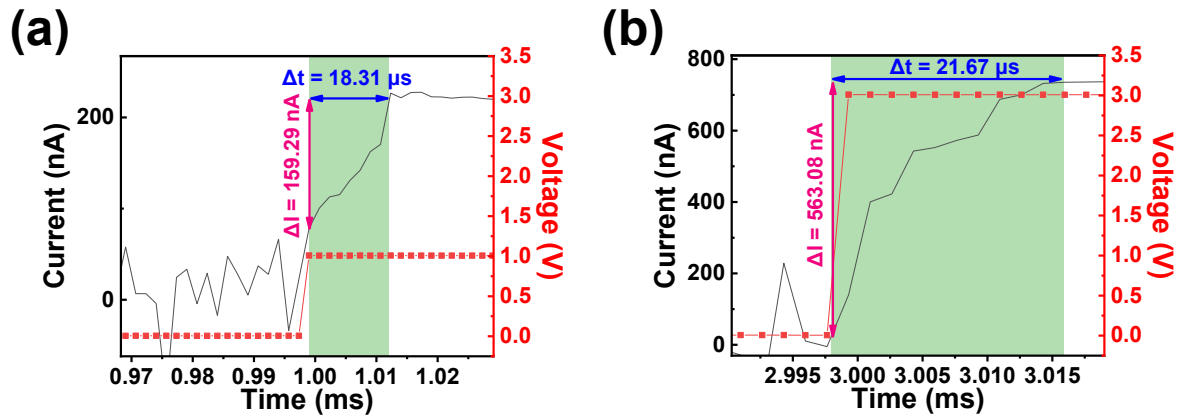


Fig. S4 Energy consumption of the Pt/IGZO/SnO_x/TiN device. (a) Energy consumption of read process, resulting 2.92 pJ. (b) Energy consumption of write process, resulting 36.6 pJ.

Table. S2 The key parameter comparison of Pt/IGZO/SnO_x/TiN device compared to previous works.

No	Structure	Switching film thickness	Endurance	Operating current	Power consumption	Switching type	Memory storing function	Reference
1	ITO/MoS ₂ /EGaIn	25 nm-	> 10 ³	< 0.1 A	32.9 pJ	Digital	Non-volatile	[S11]
2	VO _x /SiO ₂ /Si	140 / 2.1 nm	N/A	N/A	0.53 pJ	Digital	Non-volatile	[S12]
3	Au/MoS ₂ /Au	N/A	> 400	< 1 mA	200 pJ	Digital	Non-volatile	[S13]
4	Pt/ZrO ₂ /IGZO/TiN	3 / 5 nm	> 100	< 4 mA	4 μJ	Digital	Non-volatile	[S14]
5	Ag/SiC/Pt	10 nm	> 100	< 10 μA	32.25 pJ	Digital	Non-volatile	[S4]
6	Ag/MoS ₂ /Pt	30 nm	> 10 ⁶	N/A	400 nJ	Digital	Non-volatile	[S15]
7	Ag/Ti ₃ C ₂ T _x NS/Pt	N/A	N/A	< 50 μA	18.82 nJ	Digital	Volatile	[S16]

8	TiN/SiO ₂ /TaO _x /Pt	25 / 2 nm	N/A	< 1 mA	N/A	Digital	Non-volatile	[S17]
9	Ti/TaO _x /ITO	10 nm	> 10 ³	< 10 mA	N/A	Digital	Non-volatile	[S9]
10	Pt/IGZO/SnO _x /TiN	10 / 5 nm	> 100	< 500 μA	36.6 pJ	Analog	Volatile	This work

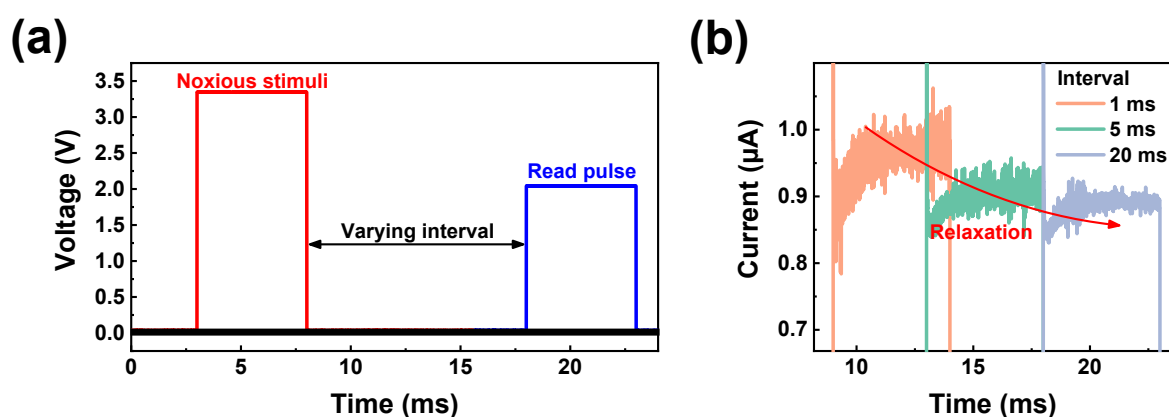


Fig. S5 (a) Pulse schematic utilized to gain the relaxation properties of the Pt/IGZO/SnO_x/TiN device. (b) Current response at different relaxation periods.

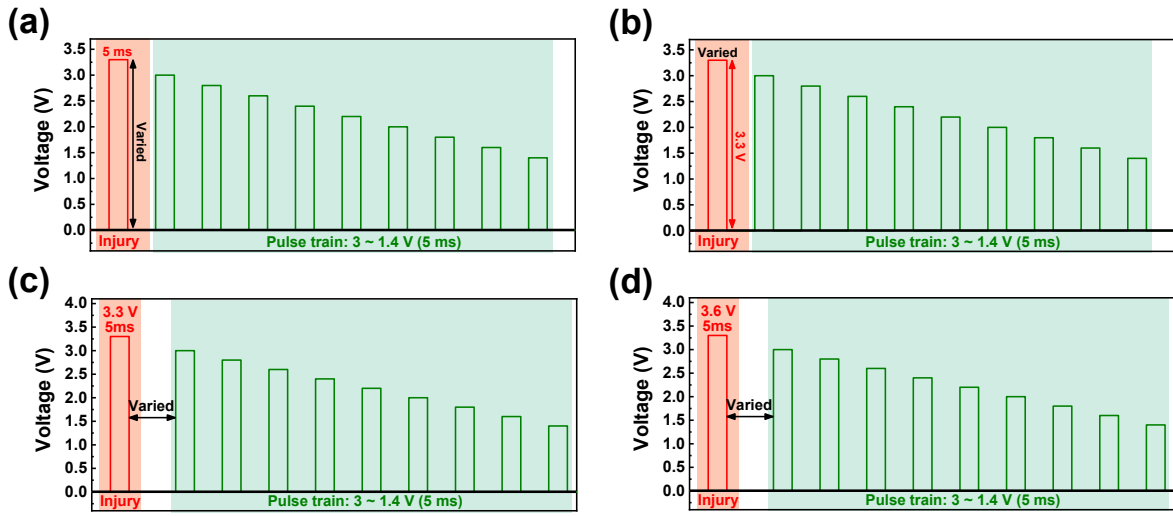


Fig. S6 Pulse schematic utilized to gain sensitization properties of the Pt/IGZO/SnO_x/TiN device, earned through differing (a) injury amplitude, and (b) injury width. Pulse schematic utilized to observe recovery under injury of (c) 3.3 V, and (d) 3.6 V.

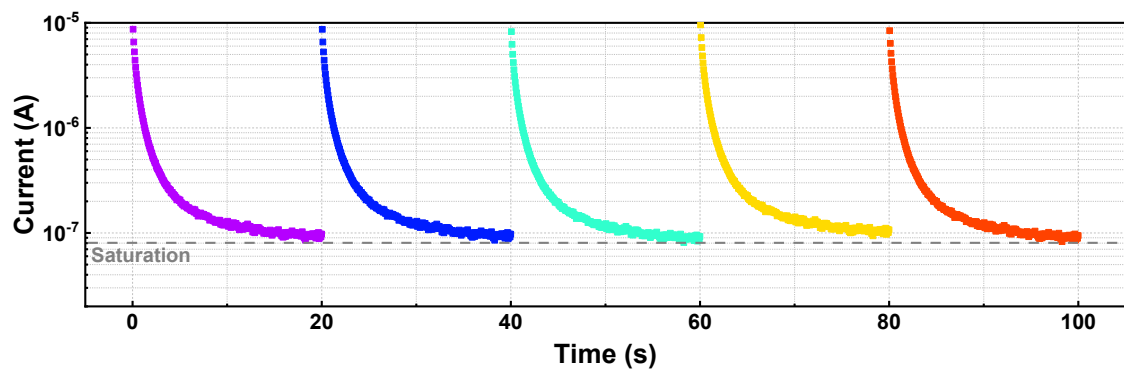


Fig. S7 Repeated LTM transition behavior under application of 50 set pulses.

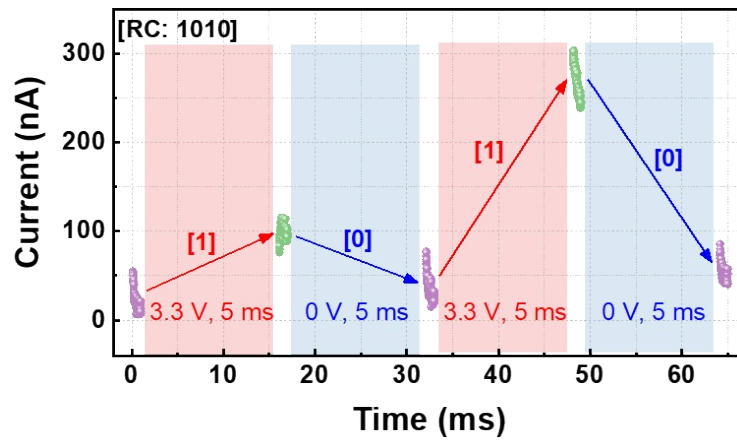


Fig. S8 Schematic illustration of current response of 4-bit reservoir computing following the use of a [1010] pulse.

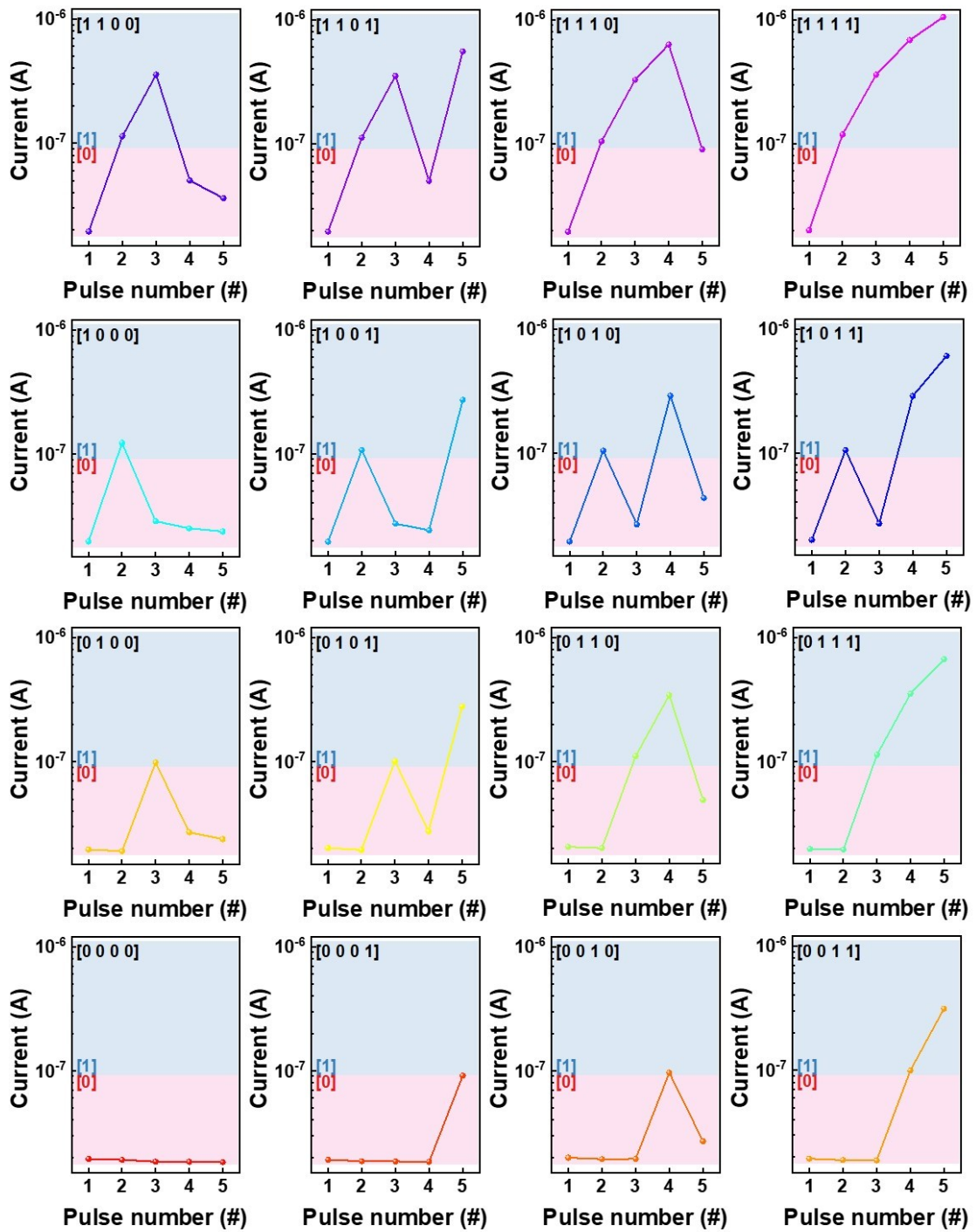


Fig. S9 Sixteen different reservoir states of the 4-bit reservoir computing based on Pt/IGZO/SnO_x/TiN device.

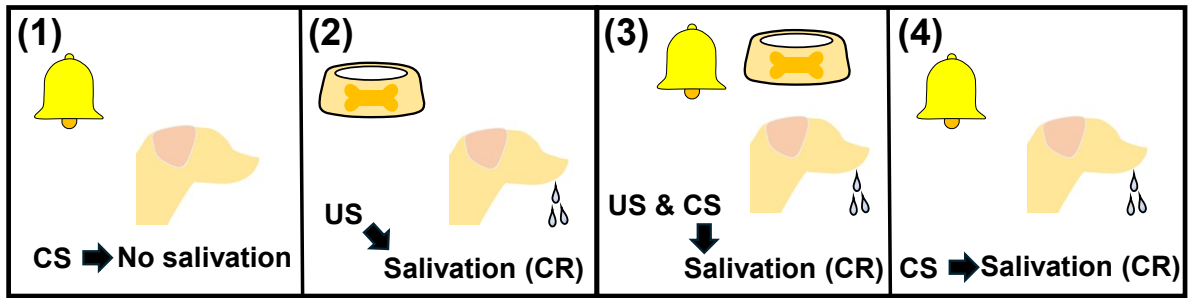


Fig. S10 Schematic illustration of Pavlovian conditioning process.

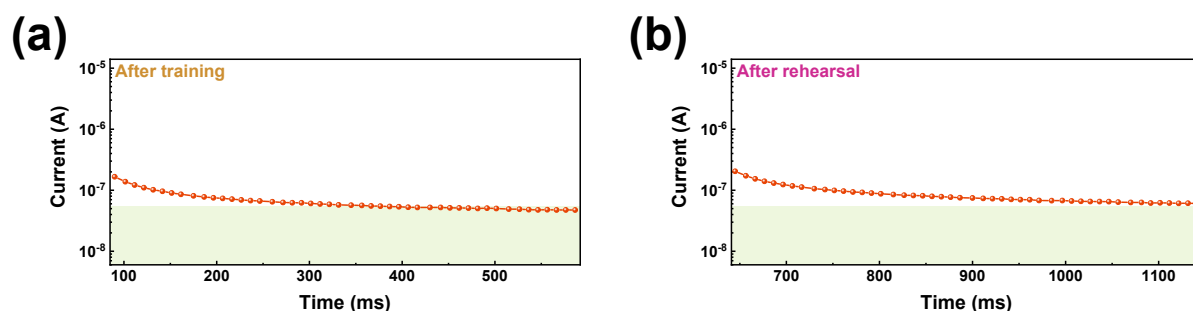


Fig. S11 Facilitation of current observed from Pavlovian conditioning for 500 s after (a) training, and (b) rehearsal.

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