

## Cobalt-doped Zinc Oxide Based Memristors with Nociceptor Characteristics for Bio-inspired Technology

Naveed Ur Rehman<sup>a</sup>, Aziz Ullah<sup>a</sup>, Muhammad Adil Mahmood<sup>a</sup>, Nasir Rahman<sup>b</sup>, Mohammad Sohail<sup>b</sup>, Shahid Iqbal<sup>c</sup>, Nizomiddin Juraev<sup>d,e</sup>, Khaled Althubeitf, Sattam Al Otaibi<sup>g</sup>  
Rajwali Khan<sup>a,h</sup>,

---

*a. Department of Physics, University of Lakki Marwat, Lakki Marwat, 2842, KP, Pakistan Address here.*

*b. Department of Physics, University of Wisconsin, Madison, WI, US.*

*c. Researcher, Faculty of Chemical Engineering, New Uzbekistan University, Tashkent, Uzbekistan*

*d. Researcher, Faculty of Chemical Engineering, New Uzbekistan University, Tashkent, Uzbekistan.*

*e. Scientific and Innovation Department, Tashkent State Pedagogical University, Tashkent, Uzbekistan.*

*f. Department of Chemistry, College of Science, Taif University, P.O. BOX. 110, 21944 Taif, Saudi Arabia*

*g. Department of Electrical Engineering, College of Engineering Taif University, P.O. Box 11099, Taif 21944, Saudi Arabia*

*h. Department of Physics United Arab Emirates University Al Ain 15551, United Arab Emirates*

† **Corresponding author: rajwalipak@zju.edu.cn.**

### Introduction

The goal of neuromorphic computing, which seeks to develop hardware circuits that can process data similarly to the human brain, has seen a major push in recent years [1, 2]. The neuromorphic chip is a type of energy-efficient information processing technology that draws inspiration from the brain to do complex tasks. The algorithms and physical implementation of computing systems that are created by humans differ greatly from those found in brains. Both the inference phase, which is when a network is given an input and computes the outcome, and the training phase might be sped up with neuromorphic circuits. Constructing systems that function with the current generation of most powerful neural networks' hierarchical layered architecture, where the synaptic inputs of the next layer are naturally fed by the neuron outputs of the previous layer, is a problem. Vast electrical energy is needed to process vast volumes of data. Furthermore, the issue gets substantially worse when artificial intelligence (AI) and its relatives machine learning and deep learning are involved. All of this might change with the development of neuromorphic chip architectures. These chips can perform numerous jobs at once and handle difficult tasks efficiently and effectively because they are particularly intended to mimic the functions of the human brain [3]. The demand for other strategies and technologies that can offer improved capabilities above and beyond what conventional CMOS technology can supply is growing as AI develops and Moore's Law encounters limitations in order to serve the changing requirements of AI applications [4, 5]. Memristor-based nanoelectronic devices that concurrently

## Supporting Information

implement data processing and storage enable the construction of matching computer architectures for artificial intelligence systems that go beyond the von Neumann bottleneck. For a wide variety of synapses, neural networks, neuromorphic computing, AI systems, and computer chips for AI applications, memristors exhibit considerable potential [6-8]. One of the ongoing challenges in AI systems is figuring out how to create a memristor-based system with integrated brain-like multifunctional applications. Currently, memristor-based artificial intelligence systems only perform tactile and visual tasks; additional human brain-like capabilities remain uncommon [9].

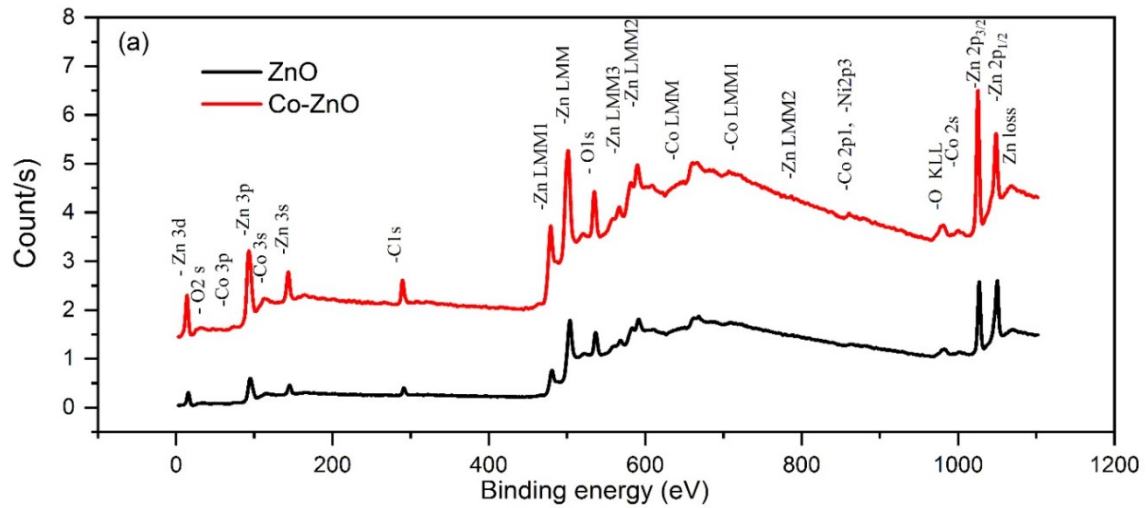


Figure-S-1. (Supporting information) XPS analysis of ZnO and Co-doped ZnO films

## Supporting Information

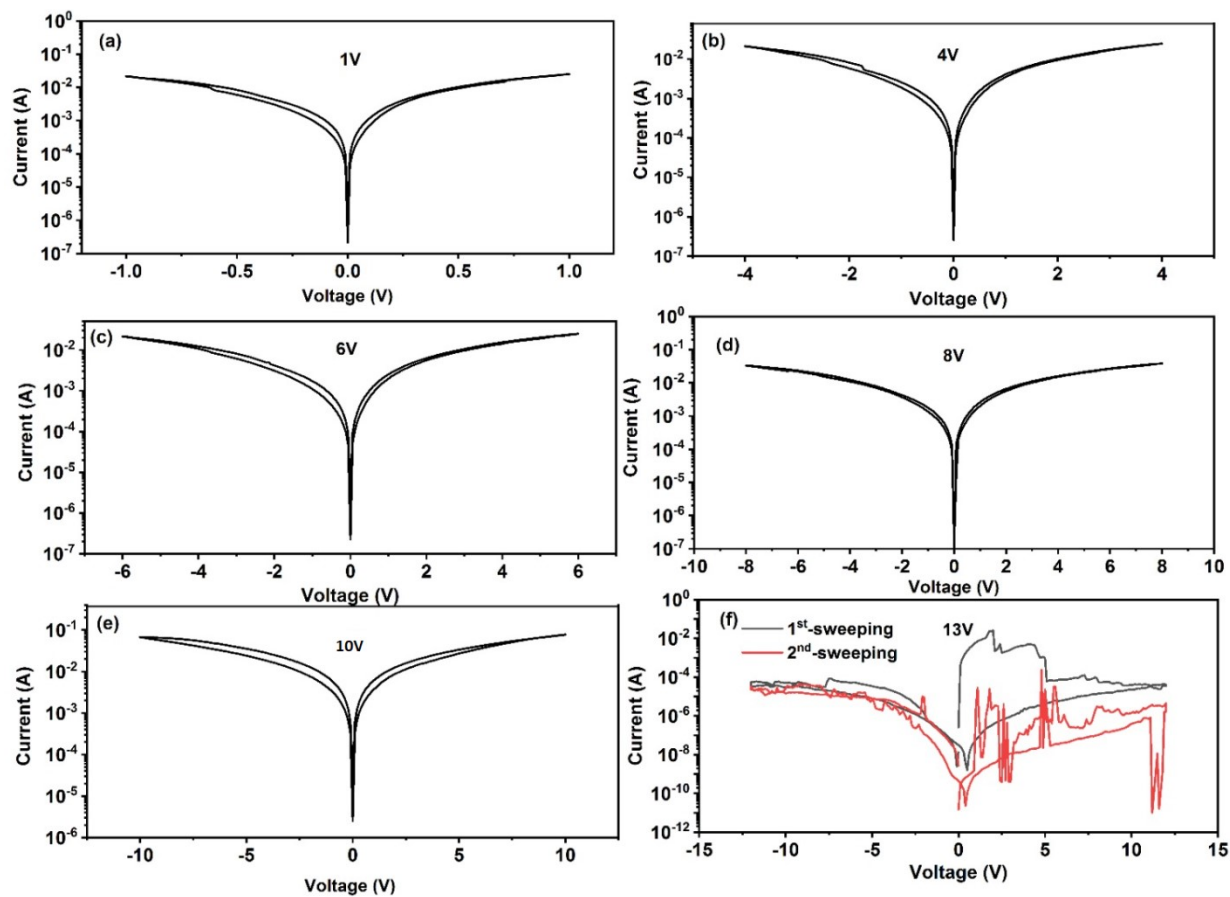
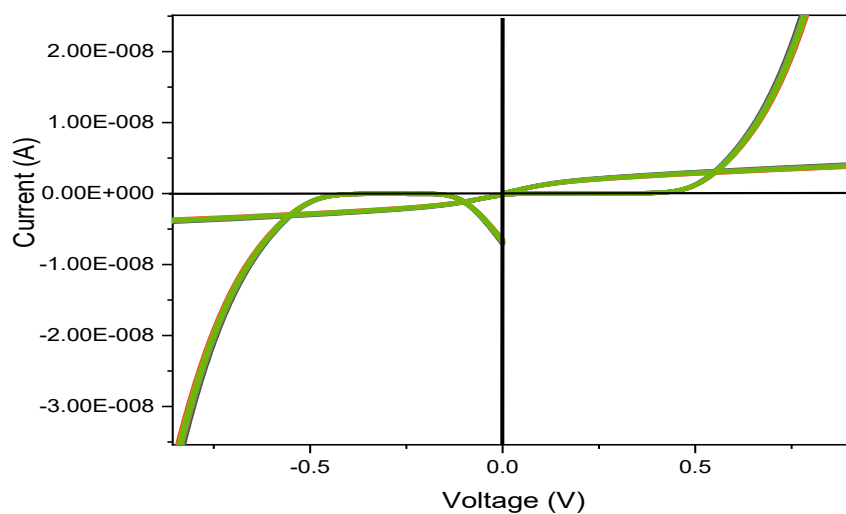


Figure-S-2. Sweeping behavior for P<sup>++</sup>-Si/Zno/Au under sweeping voltages. (a) 1 V, (b) 4 V, (c) 6 V, (d) 8V, (e) 10 V. (f) 13 V, on a semi-logarithmic scale.



S3: The enlarged part of the I-V showing non-zero crossing.

## Supporting Information

1. Bengio, Y., Y. Lecun, and G. Hinton, *Deep learning for AI*. Communications of the ACM, 2021. **64**(7): p. 58-65.
2. Furber, S., *Large-scale neuromorphic computing systems*. Journal of neural engineering, 2016. **13**(5): p. 051001.
3. Duan, X., et al., *Memristor - Based Neuromorphic Chips*. Advanced materials, 2024: p. 2310704.
4. Khan, H.N., D.A. Hounshell, and E.R. Fuchs, *Science and research policy at the end of Moore's law*. Nature Electronics, 2018. **1**(1): p. 14-21.
5. Theis, T.N. and H.-S.P. Wong, *The end of moore's law: A new beginning for information technology*. Computing in science & engineering, 2017. **19**(2): p. 41-50.
6. Norambuena, A., et al., *Polariton-based quantum memristors*. Physical Review Applied, 2022. **17**(2): p. 024056.
7. Sun, B., et al., *ABO<sub>3</sub> multiferroic perovskite materials for memristive memory and neuromorphic computing*. Nanoscale Horizons, 2021. **6**(12): p. 939-970.
8. Wang, M., et al., *Tactile near - sensor analogue computing for ultrafast responsive artificial skin*. Advanced Materials, 2022. **34**(34): p. 2201962.
9. Sun, B., et al., *Memristor-Based Artificial Chips*. ACS nano, 2023. **18**(1): p. 14-27.