

Supplemental Information

Near Room Temperature Multilevel Resistive Switching Memory with Thin Film Ionic Liquid Crystal

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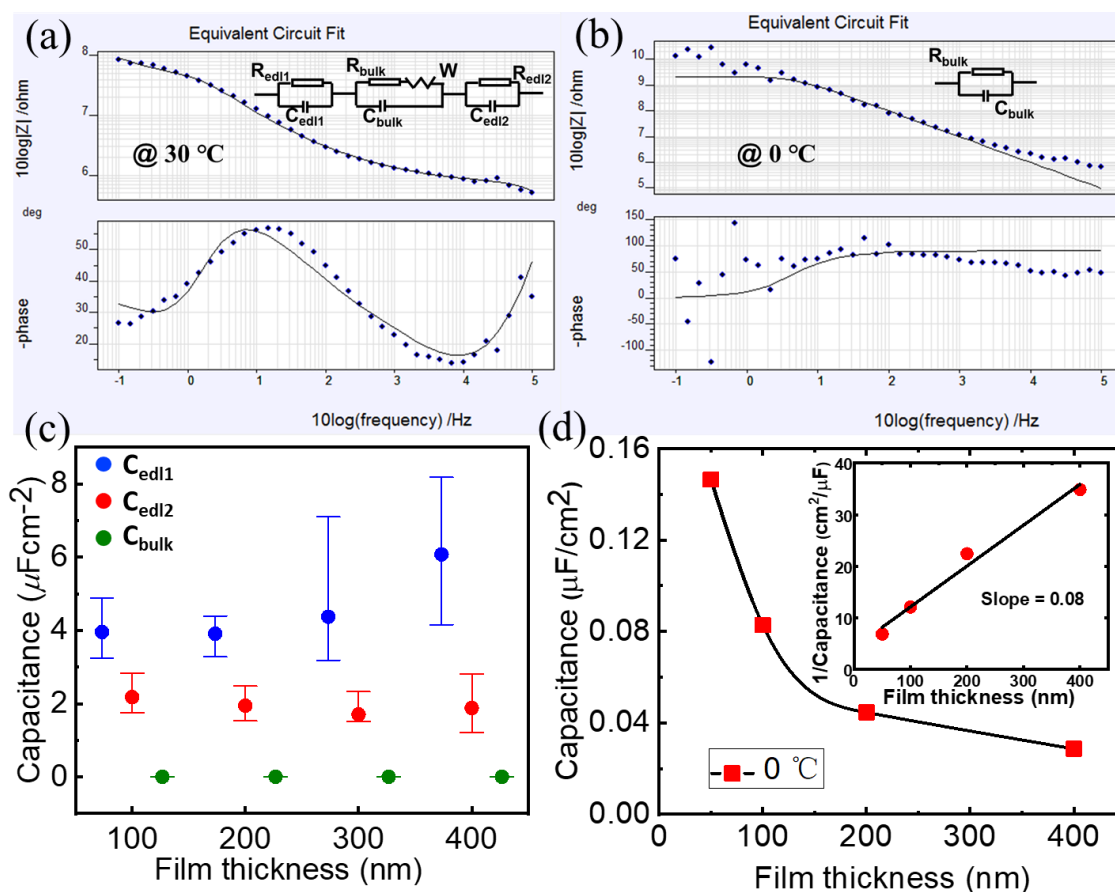


Figure S1. Bode plots of EIS for the Ag/[C₁₂mim][BF₄]/H-Si(100) devices taken without bias voltages at (a) 30 °C and (b) 0 °C. The blue diamonds and the black solid lines indicate the experimental data and their fittings, respectively. The insets are the equivalent circuit models used for

the fitting, where one model including two parallel RC circuits and one parallel RC circuit with Walburg component (W) in series was for the fitting at 30 °C, the other model with one parallel RC circuit was used at 0 °C. The values of C_{edl1} and C_{edl2} at 30 °C are 2.37 and 2.67 $\mu\text{F}/\text{cm}^2$, respectively, which are acceptable values for the EDL capacitance, with a C_{bulk} of 0.006 $\mu\text{F}/\text{cm}^2$ which can be ignored. In contrast, the value of C_{bulk} at 0 °C is 0.05 $\mu\text{F}/\text{cm}^2$, which is too small as the EDL and is only a value of bulk insulator capacitance, indicating that the EDL was not formed at 0 °C. Thickness dependence of the capacitance measured at (c) 30 °C (SmA phase) and (d) 0 °C (LtSm or crystal phase) of a Ag/[C₁₂mim][BF₄]/H-Si(111) device. The inset of (d) shows a plot of the inverse of capacitance against the thickness. The fitting of the experimental data is depicted as a solid line. The capacitance was inversely proportional to the thickness at 0 °C, while the capacitance became independent of the thickness at 30 °C, indicating that an electric double layer (EDL) is formed at the electrode interfaces when thin film [C₁₂mim][BF₄] is in the SmA phase.¹ These relationship between the capacitance and ILC film thickness confirmed the formation of EDL when thin film [C₁₂mim][BF₄] is in the SmA phase.

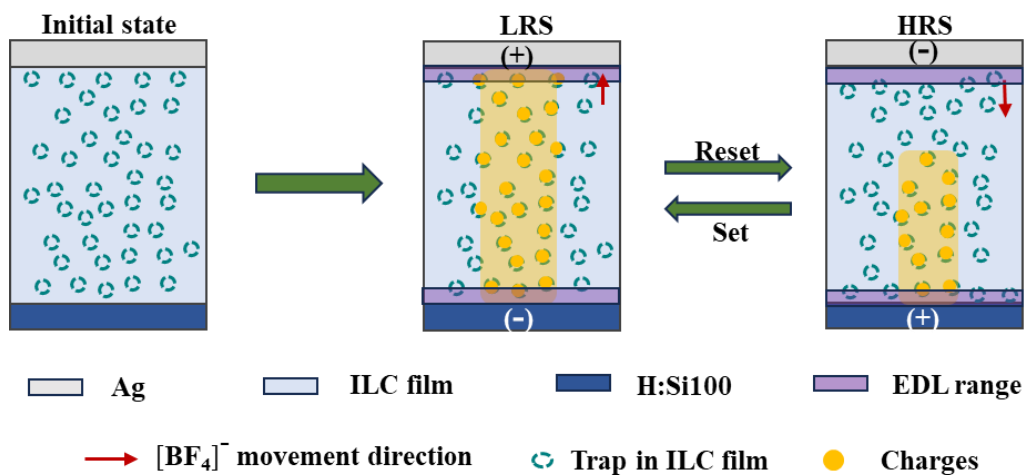


Figure S2. Schematic illustration of the device operation at 30 °C. When a positive voltage was applied between the top and bottom electrodes, the EDLs were formed at the interfaces between [C₁₂mim][BF₄] and the electrodes by the movement of ions near the interface, which would allow for

a large electrical field concentrated at the interfaces, assisting the injection of charges from the electrodes into the $[C_{12mim}][BF_4]$ layer. Once the charges are injected, they would be trapped by the ions. In the set process, the formation of CFs proceeds with the traps gradually filled under such a positive voltage applied, leading to the LRS of the device. In the reset process, as a reverse voltage is applied between the electrodes, the polarity of the EDLs at the interfaces become opposite, and the trapped charges are gradually de-trapped, with the eventual rupture of the CFs returning the device to the HRS.

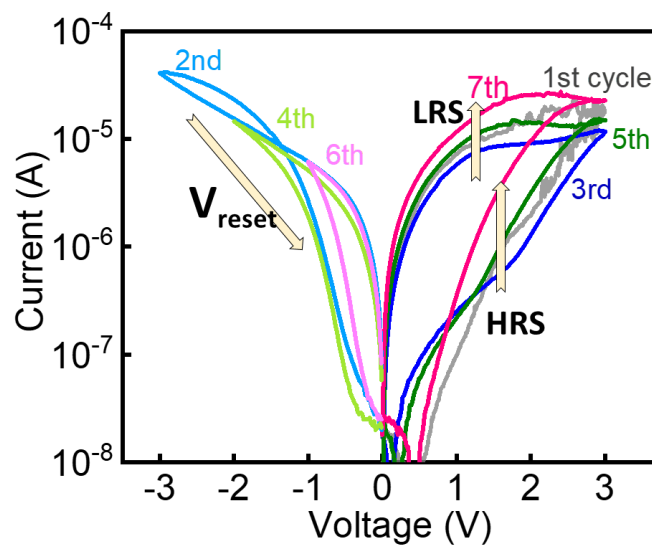


Figure S3. Reset voltage (V_{reset}) dependence of I - V curves. The order of the voltage sweep is indicated by the ordinal number along side of each curve. The currents of the HRS and LRS in the positive voltage sweeps (set process) increased as V_{reset} decreased.

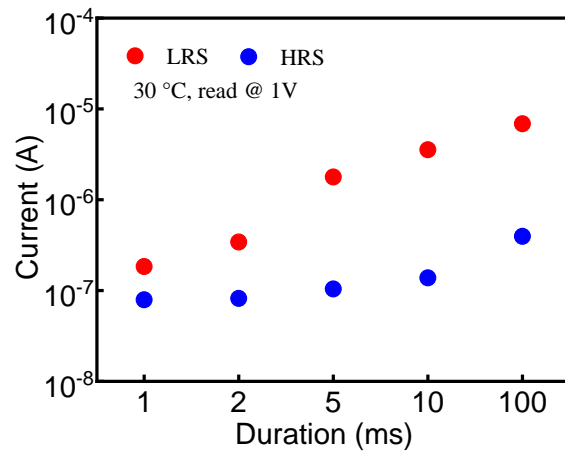


Figure S4. Duration dependence of the respective current values read at +1 V in the LRS and HRS taken from Figure 4b.

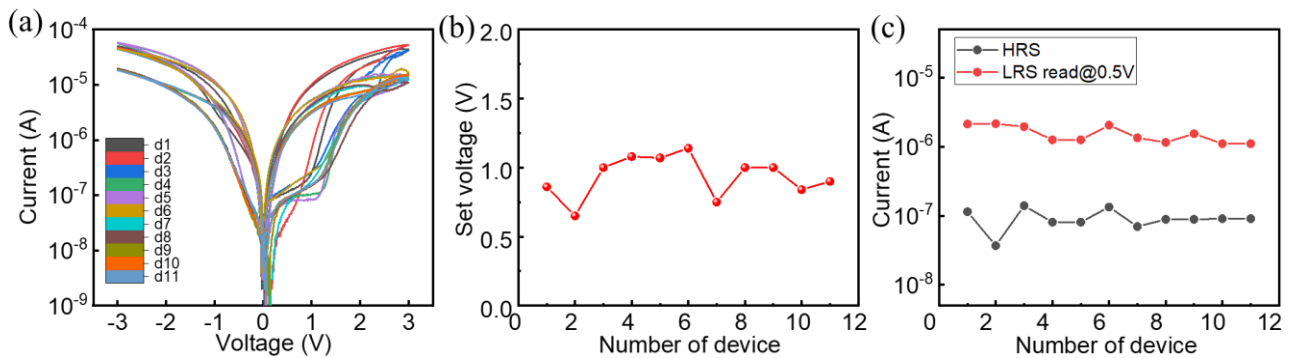


Figure S5. (a) I - V curves of randomly selected devices, (b) & (c) Variations of the set voltage, and of the current value for the LRS and HRS read at 0.5V, extracted from (a). The set voltage value was varied around 0.9 V with a standard deviation of ± 0.1 V. The respective current values at the LRS and HRS showed small variations: $1.6 \pm 0.3 \times 10^{-6}$ A for the LRS and $9.3 \pm 1.9 \times 10^{-8}$ A for the HRS.

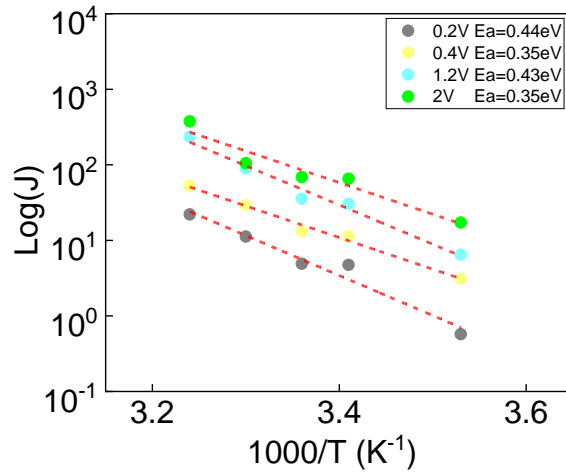


Figure S6. Arrhenius plots for the I - V curves at different voltages in Figure 3c.

Table S1. Comparison of ReRAM devices using various switching layer materials.

| Top/Bottom Electrodes | Switching layer | Switching voltage (V) | Endurance (cycles) | Retention (s) | Multilevel/Neuromorphic operation | Ref. |
|-----------------------|--|-----------------------|--------------------|---------------|-----------------------------------|-------------|
| Cu/Pt | ([C ₄ mim][Tf ₂ N]:Cu[Tf ₂ N] ₂)-HfO ₂ | 0.9 | 50 | - | - | [1] |
| Cu/Cu | [C ₄ mim][Br]:H ₂ O (PAA ⁻ | ~1 | 1000 | - | Multilevel Neuromorphic | [2] |
| Cu/Cu | Na ⁺ :H ₂ O):(NaOH) | - | 500 | 10000 | - | [3] |
| Ag/Si | [C ₁₆ mim][PF ₆] | 0.6 | 44 | 1000 | - | [4] |
| Ag/Si | [C ₁₂ mim][BF ₄] | 1 | 50 | 2000 | Multilevel | this result |
| Au/ITO | Cu ₃ (HHTP) ₂ | 1 | 10000 | 10000 | - | [5] |
| Ag/Pt | IGZO | 0.9 | 100000 | 10000 | - | [6] |
| Ag/Pt | PEO/PVK | 1 | 100 | 100000 | - | [7] |
| Ag/Ag | Ag-GeTe | 0.18 | - | 100000 | Multilevel & neuromorphic | [8] |
| Cu/Pt | HfO ₂ | 2 | - | - | - | [9] |
| TiN/Ta | Ta ₂ O ₅ | -1~-3 | - | - | Multilevel & neuromorphic | [10] |
| Ag/ITO | P3HT:PCBM | 0.5~2.9 | 100 | - | Multilevel | [11] |

Supporting References

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