

Supplementary Information

Natively oxidized 2D NbSe₂ enables ultralow-power electrical switching

Changying Xiong,^{a,b,#} Feiyu Tang,^{a,#} Meng Xu,^{c,#} Jiahao Shen,^a Yi Li,^{a,b} Kan-Hao Xue,^{a,b} Ming Xu,^{a,b,*} and Xiangshui Miao^{a,b}

^a*Wuhan National Laboratory for Optoelectronics, School of Integrated Circuits, Huazhong University of Science and Technology, Wuhan 430074, China*

^b*Hubei Yangtze Memory Laboratories, Wuhan, 430205, China*

^c*Department of Electrical and Electronic Engineering, the University of Hong Kong, Hong Kong, China*

#C.X., F.T., and M.X. contributed equally to this work.

*Authors to whom correspondence should be addressed: mxu@hust.edu.cn

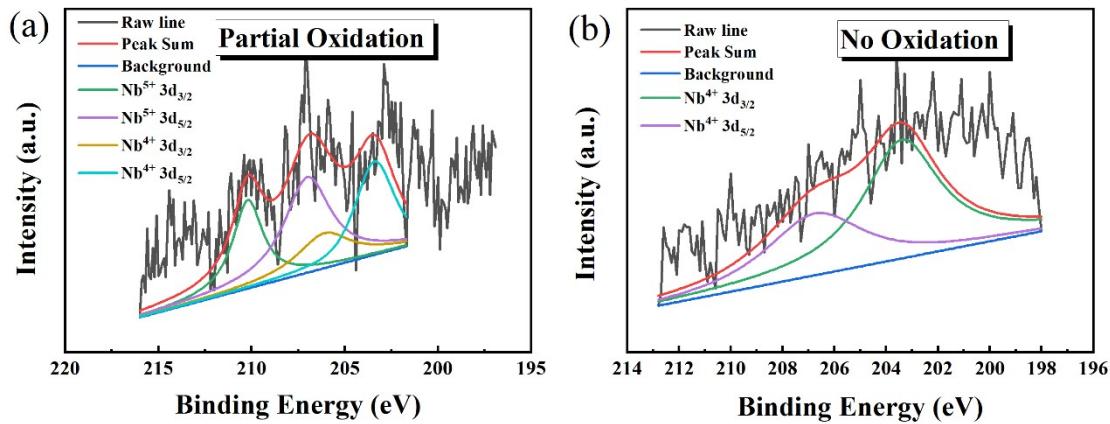


Fig. S1 XPS spectra for binding energies of NbSe_2 films. (a) In the partially oxidized sample, both Nb^{4+} and Nb^{5+} exhibit $3d_{5/2}$ and $3d_{3/2}$ peak positions. (b) In the non-oxidation sample, only the $3d_{5/2}$ and $3d_{3/2}$ peak positions of Nb^{4+} exist.

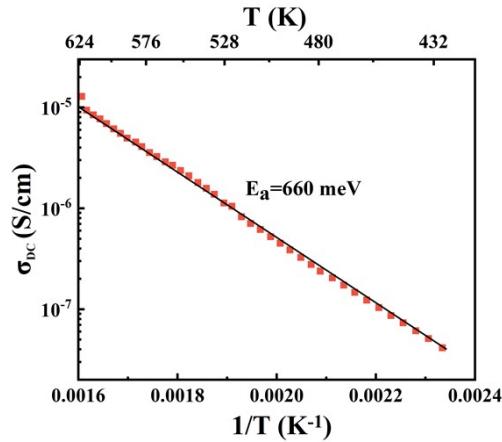


Fig. S2 Logarithm of the conductance of single crystal α -Nb₂O₅ as a function of $1/T^1$

Calculation of the functional layer resistance in devices

The calculation process is as follows:

As shown in **Fig. S2**, the relationship between resistivity and temperature in the graph can be expressed in the following equation form:

$$\lg \sigma = k \times \frac{1}{T} + b$$

Substitute the coordinates of two points into the analytical equation of a straight line:

$$(0.0016, -5), (0.0022, -7)$$

The analytical expression of the equation is obtained as:

$$\lg \sigma = -3.33 \times 10^3 \times \frac{1}{T} + 0.33$$

Set T to room temperature (T=298.15 K), the obtained result is:

$$\lg \sigma = -10.84$$

$$\sigma = 10^{-10.84} \text{ S/cm}$$

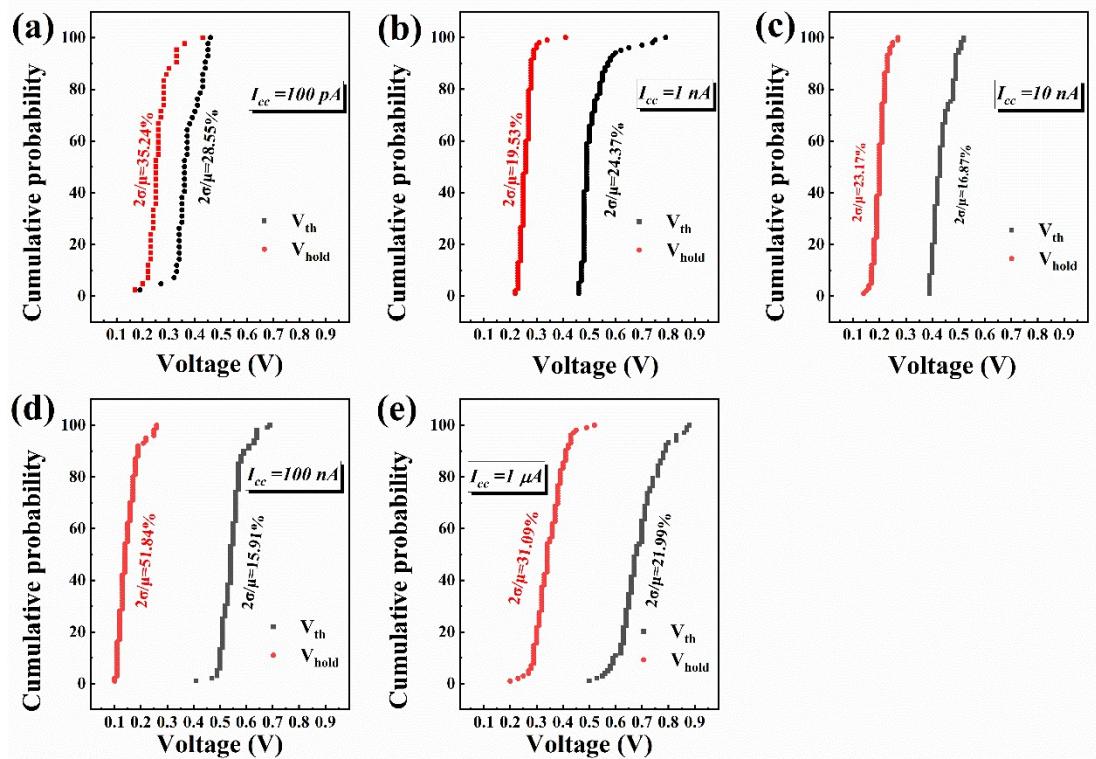


Fig. S3 The cumulative probability distribution of the device's threshold voltage and holding voltage under different compliance currents.

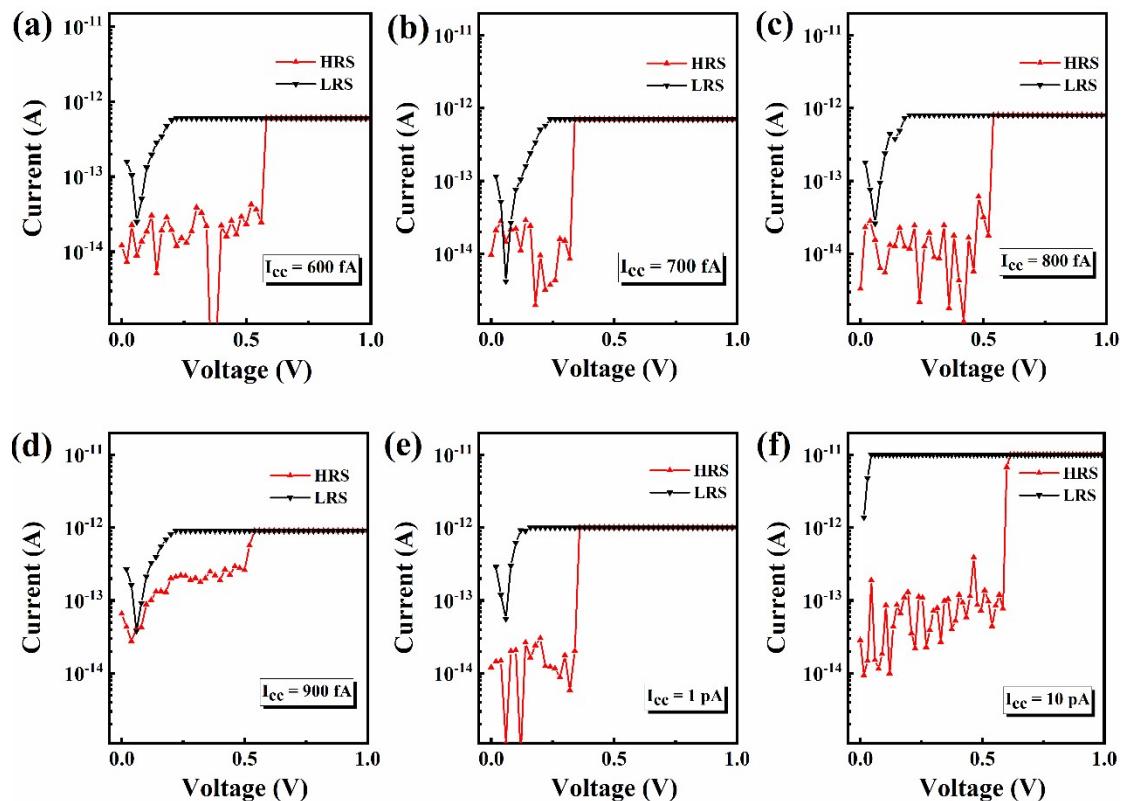


Fig. S4 The DC characteristics of the device under six different compliance currents, from 600 fA to 10 pA with the bias at 1 V.

Table S1 Comparison for similar work that utilized 2D materials or similar compounds to achieve low power consumption and high endurance.

Materials	U _{set} (V)	W _{set} (J)	Endurance	Data
DG/SiO ₂	0.675	3.5×10 ⁻¹¹	10 ⁵	Zhao ²
Nb ₂ O ₅ /NbO ₂	1.5	3.3×10 ⁻¹¹	10 ⁶	Kim ³
AgGeSe/Al ₂ O ₃	0.5	1.5×10 ⁻¹⁵	10 ⁵	Tian ⁴
TiSe ₂ /TiO ₂ /TiSe ₂	0.845	2.39×10 ⁻¹⁶	10 ²	Xiong ⁵
HfSe _x O _y /HfSe ₂	2.5	1.6×10 ⁻¹⁶	40	Liu ⁶
NbSe ₂ /NbO _x	1	/	20	Kim ⁷
Ti: NbO ₂	2.5	2.5×10 ⁻¹⁸	~10 ⁵	Jeon ⁸
pV3D3/MLG	3	>9×10 ⁻¹⁴	100	Jang ⁹
HfO _x	3.5	3.5×10 ⁻¹³	120	Wang ¹⁰
BN/SiO ₂	1.3	1.3×10 ⁻¹¹	2×10 ⁴	Ranjan ¹¹
NbSe₂/Nb₂O₅	0.5	6.3×10⁻¹⁹	10⁶	This Work

References

- 1 G. Nanda, E. W. Awin, T. Gasyak, E. Koroleva, A. Filimonov, S. Vakhrushev, R. Sujith and R. Kumar, Ceram. Inter., 2020, 46, 9512-9518.
- 2 X. Zhao, J. Ma, X. Xiao, Q. Liu, L. Shao, D. Chen, S. Liu, J. Niu, X. Zhang and Y. Wang, Adv. Mater., 2018, 30, 1705193.
- 3 S. Kim, X. Liu, J. Park, S. Jung, W. Lee, J. Woo, J. Shin, G. Choi, C. Cho, S. Park, D. Lee, E. J. Cha, B. H. Lee, H. D. Lee, S. G. Kim, S. Chung and H. Hwang, in 2012 2012 Symposium on VLSI Technology (VLSIT), 2012, 155-156.
- 4 T. Q. Wan, Y. F. Lu, J. H. Yuan, H. Y. Li and X. S. Miao, IEEE Electron Device Lett., 2021, 4, 613-616.
- 5 C. Xiong, Z. Yang, J. Shen, F. Tang, Q. He, Y. Li, M. Xu and X. Miao, ACS Appl. Mater. Interfaces, 2023, 15, 23371-23379
- 6 L. Liu, Y. Li, X. Huang, J. Chen, Z. Yang, K. H. Xue, M. Xu, H. Chen, P. Zhou and X. Miao, Adv. Sci., 2021, 8, 2005038.
- 7 J. E. Kim, V. Vu, T. T. H. Vu, T. L. Phan, Y. R. Kim, W. T. Kang, K. Kim, Y. H. Lee and W. J. Yu, Appl. Sci., 2020, 10, 7598.
- 8 D. S. Jeon, T. D. Dongale and T. G. Kim, J. Alloy. Compd., 2021, 884, 161041.
- 9 B. C. Jang, H. Seong, J. Y. Kim, B. J. Koo, S. K. Kim, S. Y. Yang, S. G. Im and S. Y. Choi, 2D Mater., 2015, 2, 044013.
- 10 M. Wang, H. B. Lv, Q. Liu, Y. T. Li, Z. G. Xu, S. B. Long, H. W. Xie, K. W. Zhang, X. Y. Liu, H. T. Sun, X. Y. Yang and M. Liu, IEEE Electron Device Lett., 2012, 33, 1556-1558.
- 11 H. Ranjan, C. P. Singh, V. P. Singh and S. K. Pandey, Materials Science in Semiconductor Processing, 2024, 183, 108744.