

Supplementary Materials for

High-performance visible-to-near infrared
phototransistor based on SnSe/SnS₂ van der
Waals heterostructure

Supporting Information

High-performance visible-to-near infrared phototransistor based on SnSe/SnS₂ van der Waals heterostructure

Gaoning Fan ^a, Weishuai Duan ^a, Mengjiao Dong ^a, Xueting Luo ^a, Pengyu Zhou ^{c,*}, Chun Sun ^a, Yonghui Zhang ^a, Mengjun Wang ^a, Chao Fan ^{a,b,*}

- a. State Key Laboratory of Reliability and Intelligence of Electrical Equipment, Hebei University of Technology, Tianjin, 300401, China.
- b. CNOOC RAIBORN(Tianjin)Technology Co.,Ltd, Tianjin, 300450, China.
- c. School of Science, Northeast Electric Power University, Jilin, 132012, China.

Contents list

1. OM images of the SnS₂ and SnSe single crystals.
2. Preparation of SnSe/SnS₂ heterostructure.
3. Transfer characteristic of the SnS₂-based and SnSe-based phototransistor.
4. Response time of SnS₂ phototransistor under the illumination of a 405-nm laser.
5. Enlarged $I_{ds}-V_{ds}$ curves of the SnSe/SnS₂ phototransistor.

1. OM images of the SnS₂ and SnSe single crystals.

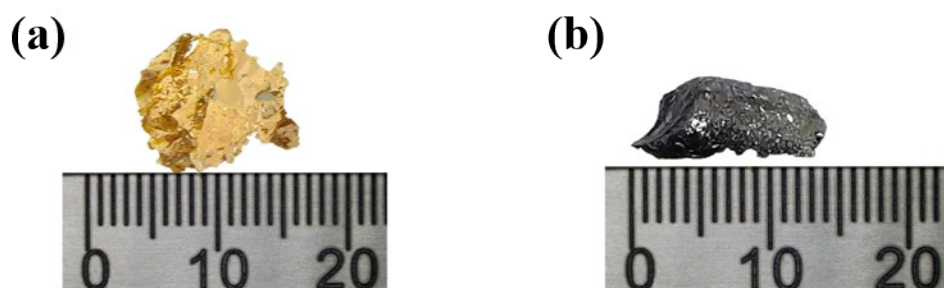


Fig. S1. OM images of the (a) SnS₂ and (b) SnSe single crystals

SnS₂ and SnSe single crystals have been grown via a CVT technique. Fig. S1 shows optical microscopy (OM) images of the (a) SnS₂ and (b) SnSe single crystals. Fig. S1 (a) shows a golden-yellow SnS₂ single crystal, which has a lateral dimension of approximately 12 mm. Fig. S1 (b) demonstrates SnSe single crystals, which can be seen to have transverse dimensions up to 11 mm and exhibit a dark grey metallic luster.

2. Preparation of SnSe/SnS₂ heterostructure.

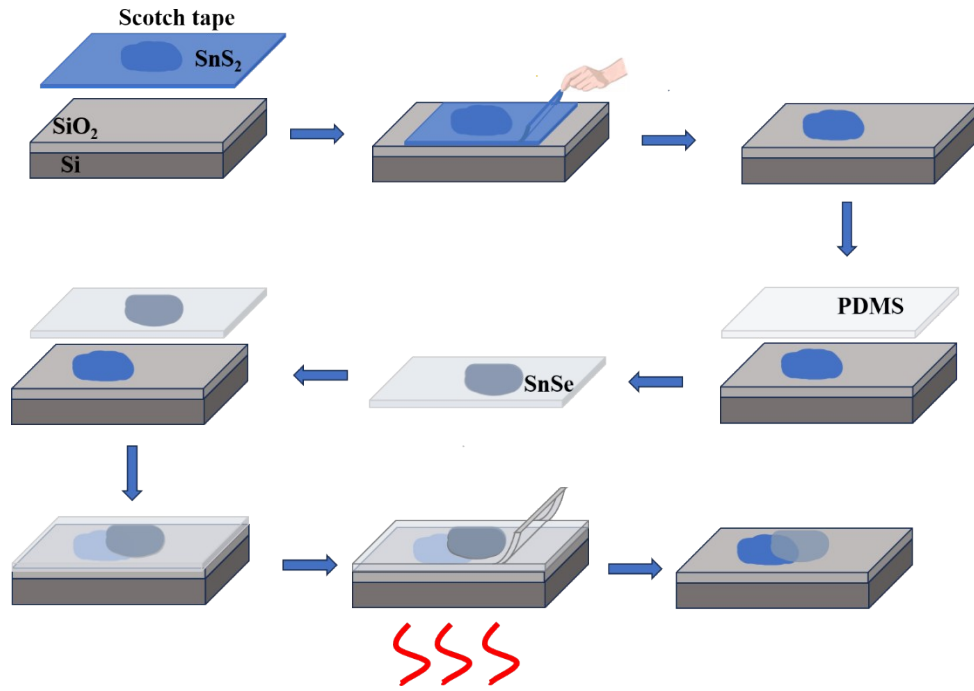


Fig. S2. Processes of the mechanical exfoliation and PDMS-assisted dry-transfer method of the SnSe/SnS₂ heterostructure

As shown in Fig. S2, SnS₂ and SnSe thin layers were mechanically exfoliated from the bulk crystals with a Scotch tape. Then the SnSe/SnS₂ heterostructure was prepared using a PDMS-assisted dry-transfer method. Firstly, a SnS₂ thin layer was exfoliated and transferred onto a SiO₂/ Si (SiO₂: 300 nm thick, Si: 400 μm thick) substrate. Secondly, a SnSe thin layer was transferred onto the above of the SnS₂ thin layer with assistance of a PDMS layer. Finally, after releasing the PDMS layer, a SnS₂/ SnSe heterostructure was formed.

3. Transfer characteristic of the SnS₂-based and SnSe-based phototransistor.

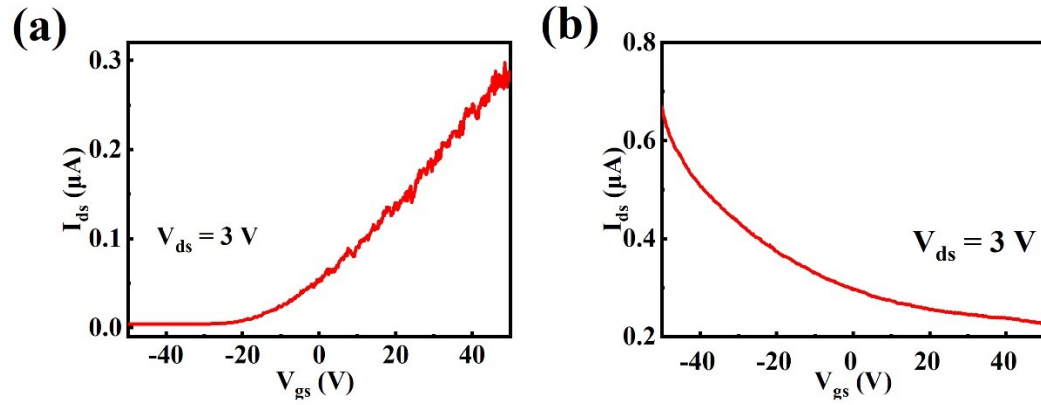


Fig. S3. Transfer characteristic of the (a) SnS₂-based and (b) SnSe-based phototransistor at $V_{ds}=3$

V

The mobility (μ) of the SnS₂-based and SnSe-based phototransistor can be determined using the following formula:

$$\mu = \frac{dI_{ds}}{dV_{gs}} \times \frac{L}{W \times C_{ox} \times V_{ds}}$$

Where the channel length L and width W are 6 and 4 μm , respectively. C_{ox} is the capacitance of the SiO₂ (11.51×10^{-5} F m⁻²). The calculated mobilities of the SnS₂-based and SnSe-based phototransistor are 0.18 cm² V⁻¹ s⁻¹ and 0.37 cm² V⁻¹ s⁻¹, respectively.

4. Response time of SnS₂ phototransistor under the illumination of a 405 nm laser.

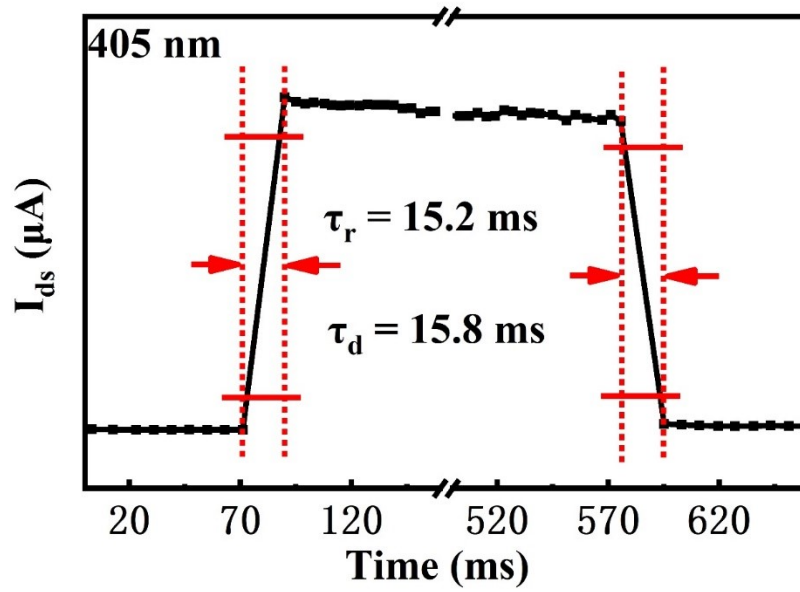


Fig. S4. Rise and decay time of SnS₂ phototransistor under 405 nm laser

As shown in Fig. S4, the rise time and decay time of the tin disulfide phototransistor under 405 nm laser illumination are 15.2 ms and 15.8 ms, respectively.

5. Enlarged $I_{ds}-V_{ds}$ curves of the SnSe/SnS₂ phototransistor.

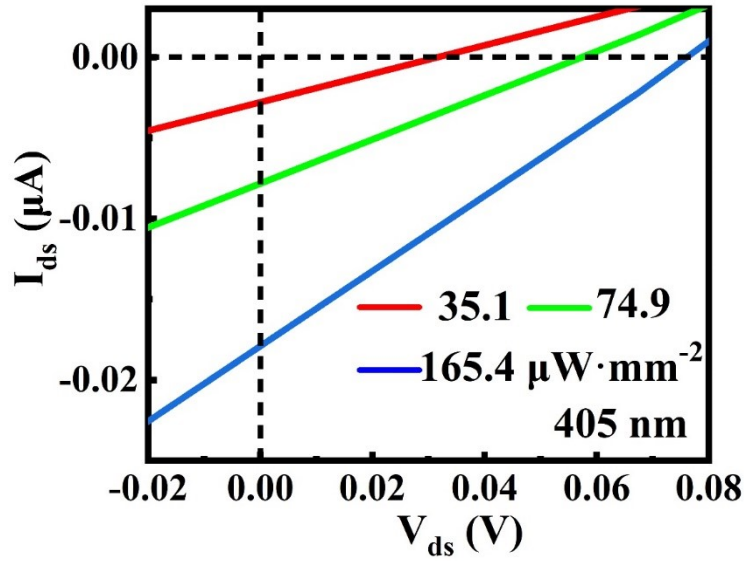


Fig. S5. Enlarged $I_{ds}-V_{ds}$ curves of the SnSe/SnS₂ phototransistor under 405 nm illumination with different light power densities.

Fig. S5 zooms in the $I_{ds}-V_{ds}$ curves of the SnSe/SnS₂ phototransistor under 405 nm illumination with different light power densities, which shows a remarkable photovoltaic behavior. I_{sc} and V_{oc} increased with the increase of light power density. Moreover, when the light power density reaches 165.4 $\mu\text{W}\cdot\text{mm}^{-2}$, the phototransistor exhibits a maximum I_{sc} and V_{oc} of 17.6 nA and 75 mV.