

## Supporting information

### Using a low temperature crystallization process to prepare anatase TiO<sub>2</sub> buffer layers for air-stable inverted polymer solar cells

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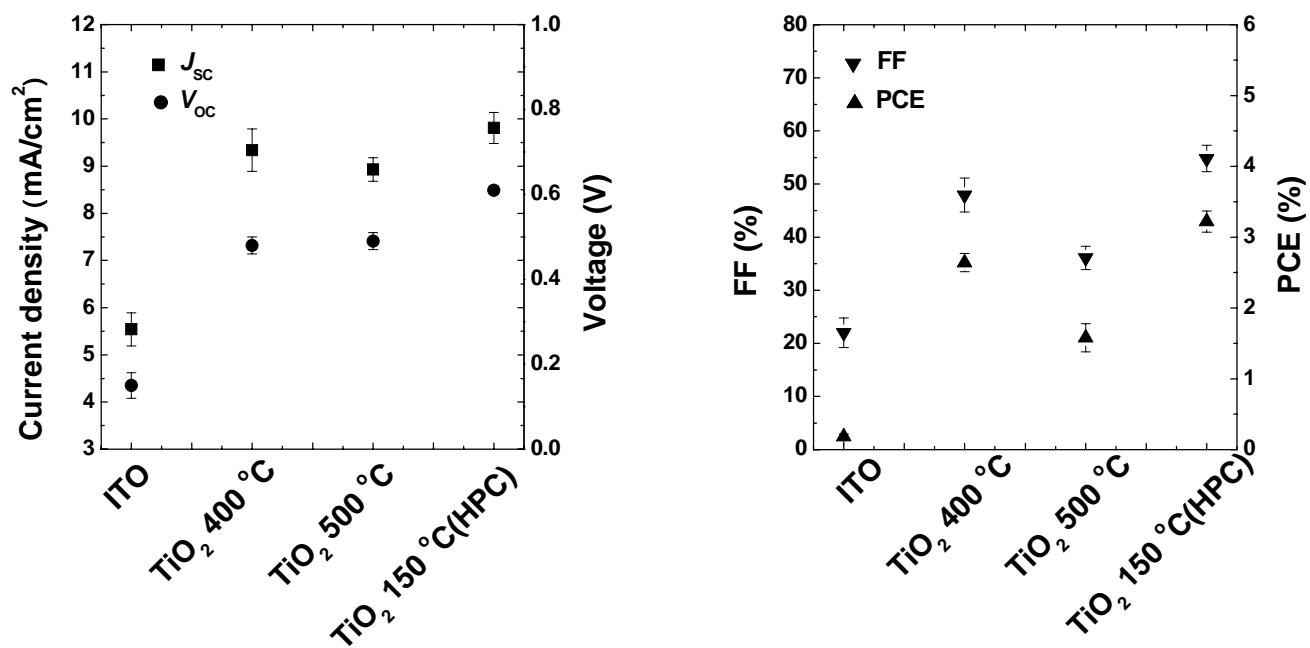


Figure S1. The performance of the BHJ devices with TiO<sub>2</sub> annealed at different temperatures.

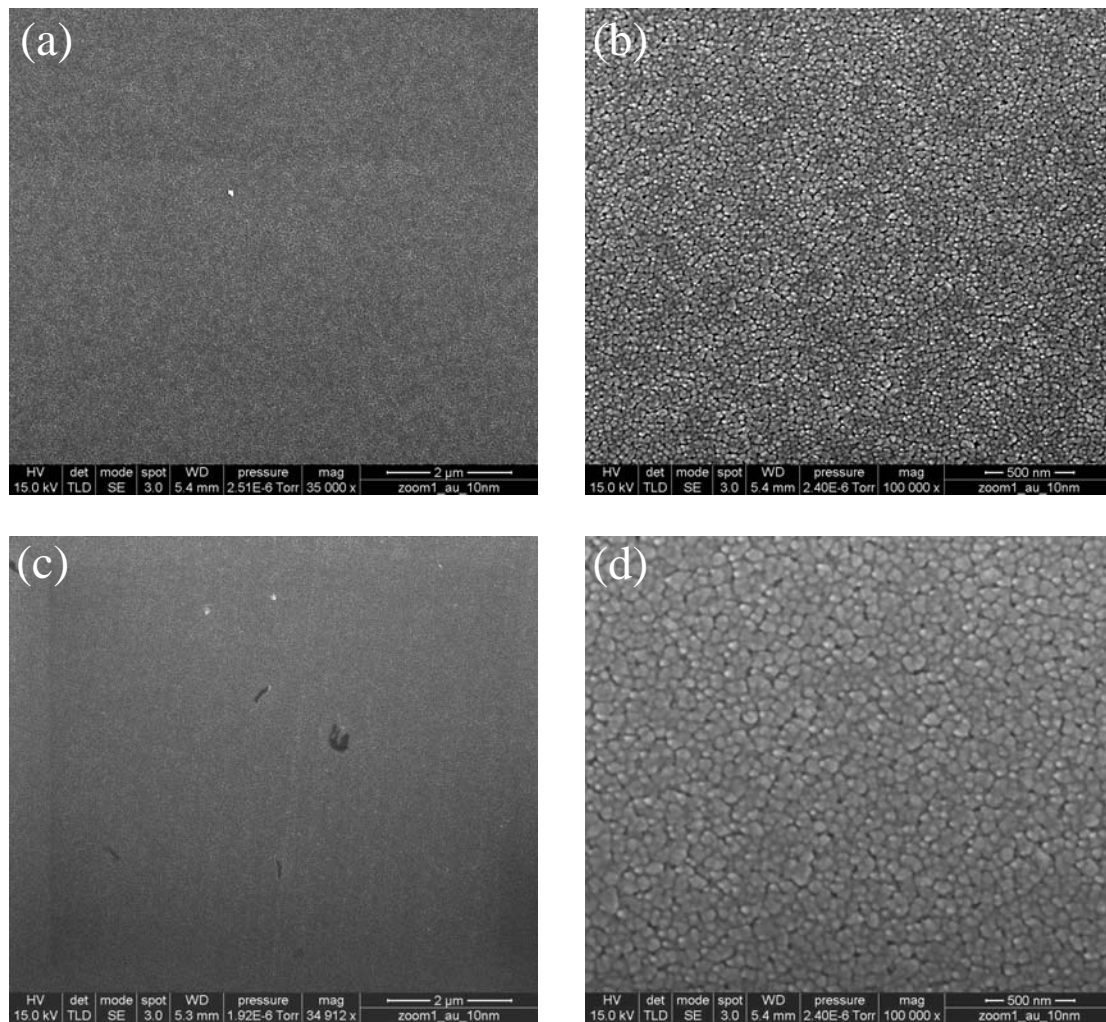


Figure S2 (a, b) the SEM images for the TiO<sub>2</sub> film annealed at 150 °C (HPC). (c, d) the SEM images for the TiO<sub>2</sub> film annealed at 500 °C.

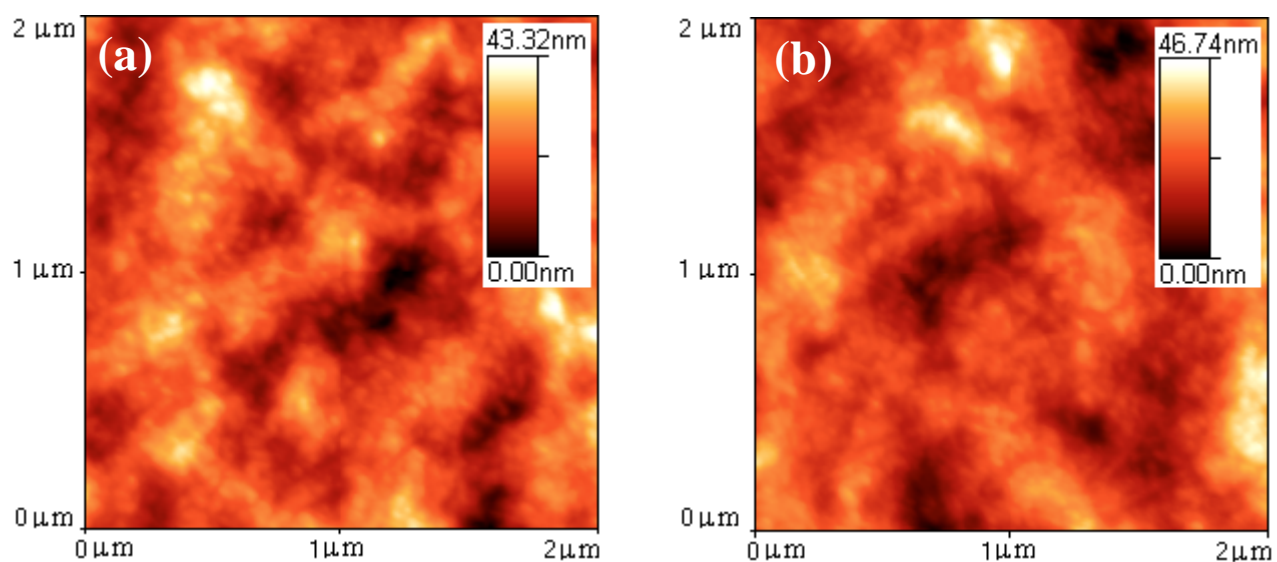


Figure S3 The AFM images of the BHJ devices fabricated with TiO<sub>2</sub> annealed at (a) 500 °C and (b) 150 °C (HPC).