Supporting Information

Low-temperature facile solution-processed gate dielectric and active channel for oxide thin film transistors

Han Wang¹, Tieyu Sun², Wangying Xu¹, Fangyan Xie³, Lei Ye¹, Yubin Xiao¹, Yu Wang², Jian Chen³, Jianbin Xu¹*

¹Department of Electronic Engineering and Materials Science and Technology Research Center, The Chinese University of Hong Kong, Shatin, N.T., Hong Kong SAR, P. R. China ²Department of Applied Physics and Material Research Center, The Hong Kong Polytechnic University, Kowloon, Hong Kong SAR, P. R. China ³Instrumental Analysis & Research Center, Sun Yat-Sen (Zhongshan) University, Guangzhou, P. R. China

jbxu@ee.cuhk.edu.hk



The performances of TFTs transfer were demonstrated in figure S1.

Figure S1 Transfer performances of the samples. The annealing temperatures of them are (a) 220 \degree , (b) 240 \degree , (c) 260 \degree , (d) 280 \degree and (e) 300 \degree .